

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of: Goren et al.
Serial No.: 10/091,934
Filed: March 6, 2002
For: AN INTERCONNECT-AWARE METHODOLOGY FOR
INTEGRATED CIRCUIT DESIGN

Group Art Unit: 2825

Examiner: LEVIN, Naum B

RULE 132 DECLARATION OF ISRAEL WAGNER AND TIBERIU C.

GALAMBOS

We, the undersigned, Israel Wagner of 6 Freud Street, Haifa, Israel, and Tiberiu C. Galambos of 12 Simtat Heharuv, Biniamina, Israel, hereby declare as follows:

1. We are co-authors of the article "An Interconnect-Aware Methodology for Analog and Mixed Signal Design, Based on High Bandwidth (over 40 GHz) On-chip Transmission Line Approach, published in Design, Automation and Test in Europe Conference and Exhibition, 2002. Proceedings 4-8 March 2002 Pages: 804-811 (hereinafter "the Publication"), which was cited against U.S. Patent Application No. 10/091,934 under 35 U.S.C. 102(a) in an Office Action dated August 8, 2006. In making this rejection, the Examiner cited subject matter from pages 1-4 of the Publication.

2. David Goren, Amir Alon, Betty Livshitz, Anatoly Sherman, Rachel Gordin, Michael Zelikson were the sole contributors of all the subject matter that was cited by Examiner in pages 1-4 of the Publication.


In Re: U.S.S.N. 10/091,934

Group Art Unit 2825

**Rule 132 Declaration of Israel Wagner and Tiberiu C. Galambos,
cont'd**

We hereby declare that all statements made herein of our own knowledge are true and that all statements made on information and conjecture are thought to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application of any patent issued thereon.

Israel Wagner, Citizen of Israel
6 Freud Street, Haifa, Israel
September 27, 2006



Tiberiu C. Galambos, Citizen of Israel
12 Simtat Heharuv, Biniamina, Israel
September 27, 2006

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Israel Wagner

Israel Wagner, Citizen of Israel
4 Floni Street, Haifa, Israel
September 23, 2006

Tiberiu C. Galambos, Citizen of _____

September 23, 2006